

PRODUCT SPECIFICATION

Part No.:	AC-XF-DG100-X	
Description:	100G QSFP28 Direct Attach Cable ,DAC 1-5m	
Release Date	Rev.	Revision Change Description
2017/06/07	A0	New Release
2020/12/28	A1	Template Update

Features

- ✧ Up to 28.3125Gbps data rate per channel
- ✧ Up to 5m transmission
- ✧ Operating temperature: 0℃~70℃
- ✧ Single +3.3V power supply
- ✧ RoHS Compliant

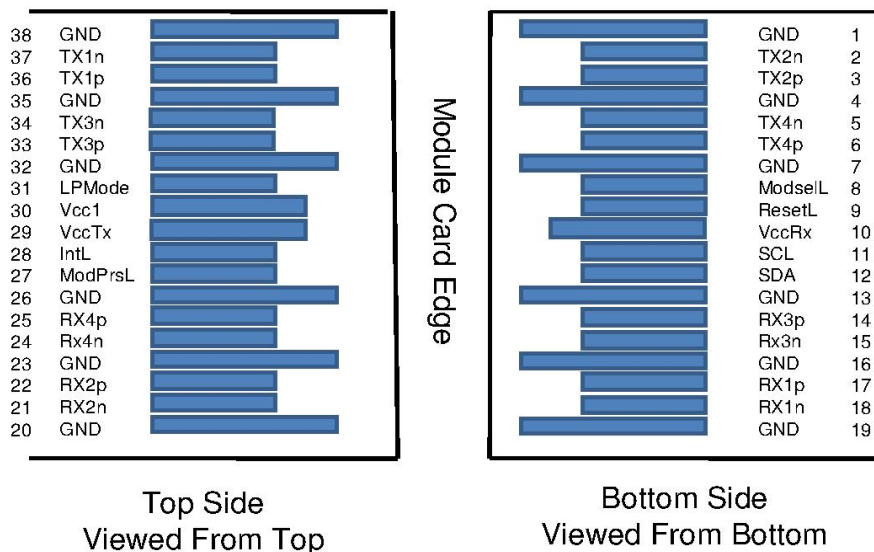
Application

- ✧ 100G Ethernet

Standard

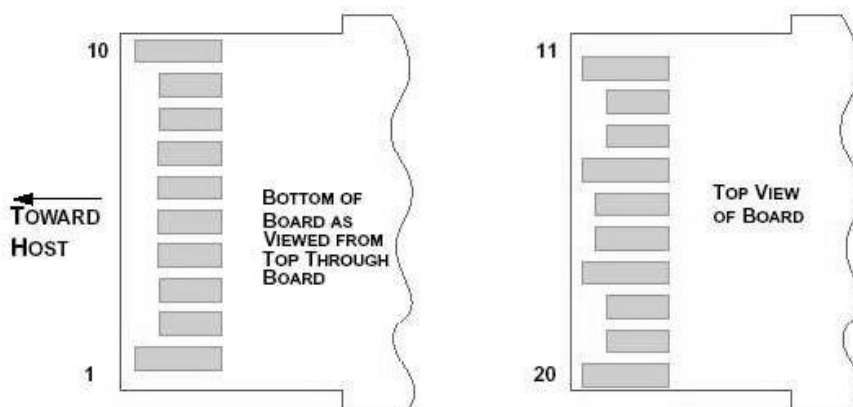
- ✧ Compliant with the SFF-8665

Pin Assignment



Pin	Logic	Symbol	Description
1		GND	Ground
2	CML-I	Tx2n	Transmitter Inverted Data Input
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input
4		GND	Ground
5	CML-I	Tx4n	Transmitter Inverted Data Input
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input
7		GND	Ground
8	LVTTL-I	ModSelL	Module Select
9	LVTTL-I	ResetL	Module Reset
10		Vcc Rx	+3.3V Power Supply Receiver
11	LVC MOS-I/O	SCL	2-wire serial interface clock
12	LVC MOS-I/O	SDA	2-wire serial interface data
13		GND	Ground
14	CML-O	Rx3p	Receiver Non-Inverted Data Output
15	CML-O	Rx3n	Receiver Inverted Data Output
16		GND	Ground
17	CML-O	Rx1p	Receiver Non-Inverted Data Output
18	CML-O	Rx1n	Receiver Inverted Data Output
19		GND	Ground
20		GND	Ground
21	CML-O	Rx2n	Receiver Inverted Data Output
22	CML-O	Rx2p	Receiver Non-Inverted Data Output

23		GND	Ground
24	CML-O	Rx4n	Receiver Inverted Data Output
25	CML-O	Rx4p	Receiver Non-Inverted Data Output
26		GND	Ground
27	LVTTL-O	ModPrsL	Module Present
28	LVTTL-O	IntL	Interrupt
29		Vcc Tx	+3.3V Power supply transmitter
30		Vcc1	+3.3V Power supply
31	LVTTL-I	LPMODE	Low Power Mode
32		GND	Ground
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input
34	CML-I	Tx3n	Transmitter Inverted Data Input
35		GND	Ground
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input
37	CML-I	Tx1n	Transmitter Inverted Data Input
38		GND	Ground



SFP+ Pin Function Definition

Pin	Logic	Symbol	Description
1		VeeT	Module Transmitter Ground
2	LVTTL-O	Tx_Fault	Module Transmitter Fault
3	LVTTL-I	Tx_Disable	Transmitter disable; Turns off transmitter laser output
4	LVTTL-I/O	SDA	2-wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i)
5	LVTTL-I/O	SCL	2-wire Serial Interface Clock (Same as MOD-DEF1 in INF-8074i)
6		Mod_ABS	Module Absent, connected to VeeT or VeeR in the module
7	LVTTL-I	RS0	Rate Select 0, optionally controls SFP+ module receiver
8	LVTTL-O	Rx_LOS	Receiver Loss of Signal Indication (In FC designated as Rx_LOS and in Ethernet designated as Signal Detect)

9	LVTTL-I	RS1	Rate Select 1, optionally controls SFP+ module transmitter
10		VeeR	Module Receiver Ground
11		VeeR	Module Receiver Ground
12	CML-O	RD-	Receiver Inverted Data Output
13	CML-O	RD+	Receiver Non-Inverted Data Output
14		VeeR	Module Receiver Ground
15		VccR	Module Receiver 3.3 V Supply
16		VccT	Module Transmitter 3.3 V Supply
17		VeeT	Module Transmitter Ground
18	CML-I	TD+	Transmitter Non-Inverted Data Input
19	CML-I	TD-	Transmitter Inverted Data Input
20		VeeT	Module Transmitter Ground

General Product Characteristics

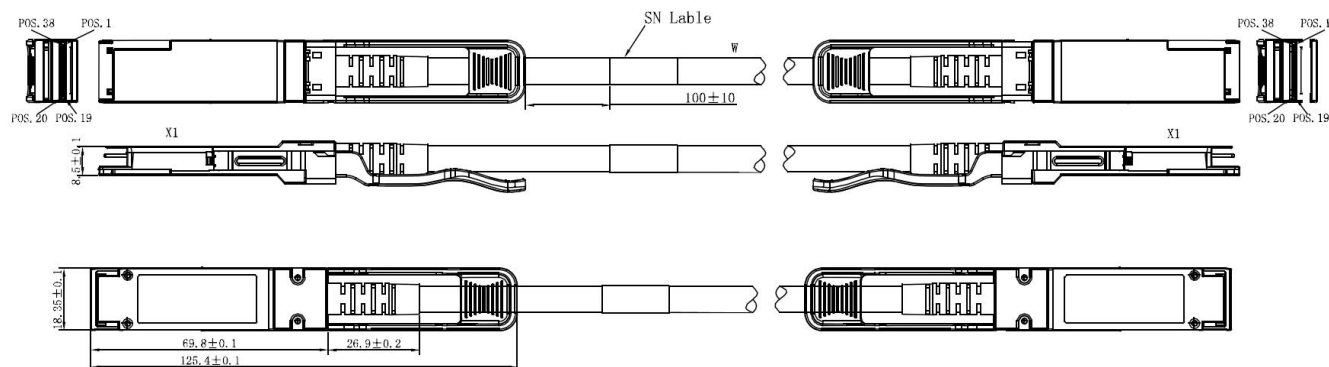
QSFP28 DAC Specifications	
Number of Lanes	Tx & Rx
Channel Data Rate	28.3125 Gbps
Operating Temperature	0 to + 70°C
Storage Temperature	-40 to + 85°C
Supply Voltage	3.3 V nominal
Electrical Interface	38 pins edge connector(QSFP+)
Management Interface	Serial, I ² C

High Speed Characteristics

Parameter	Symbol	Min	Typ	Max	Units	Notes
Differential Impedance	Zd	90	100	110	Ω	
Differential Input Return Loss	SDDXX	<-12+2* SQRT (f) with f in GHz			dB	0.01~4.1GHz
		<-6.3+13* Log10/(f/5.5) with f in GHz			dB	4.1~19GHz
Common Mode Output Return Loss	SCCXX	< -7+1.6*f with f in GHz			dB	0.01~12.89GHz
				-3	dB	12.89~19GHz
Difference Waveform Distortion Penalty	dWDPc			6.75	dB	
VMA Loss	L			4.4	dB	
VMA Loss to Crosstalk Ratio	VCR	32.5			dB	

Mechanical Specifications

The connector is compatible with the SFF-8665 specification.



Length (m)	Cable AWG
1	30
3	26
5	26/24

Ordering information

Part Number	Product Description
AC-XF-DG100-X	100G QSFP28 to QSFP28,DAC 1-5meters

Notes:

X=01,02,03meters, please contact LONTE for any specific length.