

PRODUCT SPECIFICATION

Part No.:	AC-QS-8G200-01	
Description:	200G QSFP56 SR4 Transceiver, MMF 850nm 100m	
Release Date	Rev.	Revision Change Description
2021/03/30	A0	New Release
2025/12/26	A1	1. Update the Part No from QP to QS 2. Sen changed from -7 to -6.5

Features

- ✧ Supports 212.5Gb/s PAM4
- ✧ Digital Diagnostics Monitoring Interface
- ✧ Built-in 200G PAM4 DSP/CDR
- ✧ Up to 100m reach over OM4 MMF with KRP4 FEC
- ✧ Hot-pluggable QSFP56 form factor
- ✧ Uncooled 4 channels 850nm VCSEL array
- ✧ 4 channels PIN photodetector array
- ✧ Commercial operating case temperature range: 0~ 70°C
- ✧ RoHS-6 compliant
- ✧ Power dissipation< 6W
- ✧ TDECQ< 4.5dB
- ✧ Single MPO-12 receptacle

Application

- ✧ 200G Ethernet
- ✧ Data Center
- ✧ InfiniBand HDR
- ✧ Fibre Channel

Standard

- ✧ IEEE 802.3cd 200GE SR4
- ✧ QSFP MSA
- ✧ SFF-8636

Specification:

Absolute Maximum Ratings				
Parameter	Symbol	Min	Max	Unit
Storage Ambient Temperature	T _{STG}	-20	85	°C
Operating Humidity	H _o	5	85	%
Power Supply Voltage	V _{cc}	-0.3	3.6	V
Signal Input Voltage		V _{cc} -0.3	V _{cc} +0.3	V

Recommended Operating Conditions					
Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	T _c	0		70	°C
Power Supply Voltage	V _{cc}	3.13	3.3	3.47	V
Data Rate,each Lane (PAM4)			53.125		Gbps
Fiber Length 50/125μm core MMF		-	-	100	m

Optical transmitter Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Launched Power (avg.) Per Lane	P _{avg}	-6.5		4	dBm	
Wavelength Range	λ ₀	840	850	860	nm	
Spectral Width(-20dB)	Δλ			0.6	nm	
Extinction Ratio	ER	3			dB	
Transmitter OFF Output Power	P _{Off}			-30	dBm	
Optical Modulation Amplitude(OMA outer)	OMA	-4		3	dBm	
Transmitter and dispersion eye closure(TDECQ)	TDECQ			4.5	dB	

Optical receiver Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Receiver Wavelength Range		840	850	860	nm	
Average Receiver Power Per Lane		-8.4		4	dBm	
Receiver Sensitivity Per Lane	Sen			-6.5	dBm	1
Optical Power Input Overload	P _{in-max}	4			dBm	
Receiver Reflectance	R _r			-12	dB	

Notes:

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1. Suggested < 700mVpp input differential signal for better BER performance

Pin Definition

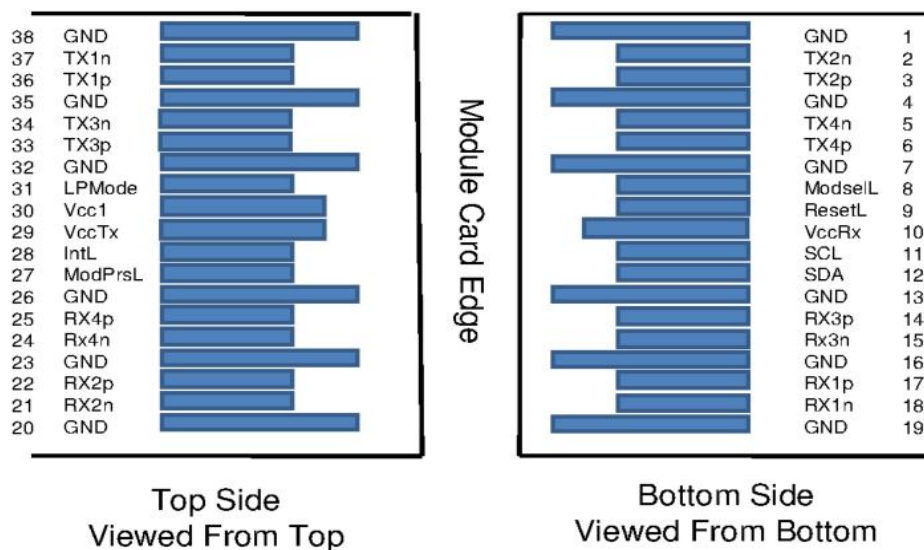


Figure1 QSFP MSA-compliant 38-pin connector

Pin	Symbol	Name/Description	Notes
1	GND	Transmitter Ground (Common with Receiver Ground)	1
2	TX2N	Transmitter Inverted Data Input	
3	TX2P	Transmitter Non-Inverted Data Input	
4	GND	Ground	1
5	TX4N	Transmitter Inverted Data Input	
6	TX4P	Transmitter Non-Inverted Data Input	
7	GND	Ground	1
8	ModSelL	Module Select	
9	ResetL	Module Reset	
10	Vcc Rx	+3.3 V Power supply receiver	2
11	SCL	2-wire serial interface clock	
12	SDA	2-wire serial interface data	
13	GND	Ground	
14	RX3P	Receiver Non-Inverted Data Output	
15	RX3N	Receiver Inverted Data Output	
16	GND	Ground	1
17	RX1P	Receiver Non-Inverted Data Output	
18	RX1N	Receiver Inverted Data Output	
19	GND	Ground	1

20	GND	Ground	1
21	RX2N	Receiver Inverted Data Output	
22	RX2P	Receiver Non-Inverted Data Output	
23	GND	Ground	1
24	RX4N	Receiver Inverted Data Output	1
25	RX4P	Receiver Non-Inverted Data Output	
26	GND	Ground	1
27	ModPrsL	Module Present	
28	IntL	Interrupt	
29	Vcc Tx	+3.3 V Power supply transmitter	2
30	Vcc1	+3.3 V Power Supply	2
31	LPMode	Low Power Mode	
32	GND	Ground	1
33	TX3P	Transmitter Non-Inverted Data Input	
34	TX3N	Transmitter Inverted Data input	
35	GND	Ground	1
36	TX1P	Transmitter Non-Inverted Data Input	
37	TX1N	Transmitter Inverted Data input	
38	GND	Ground	1

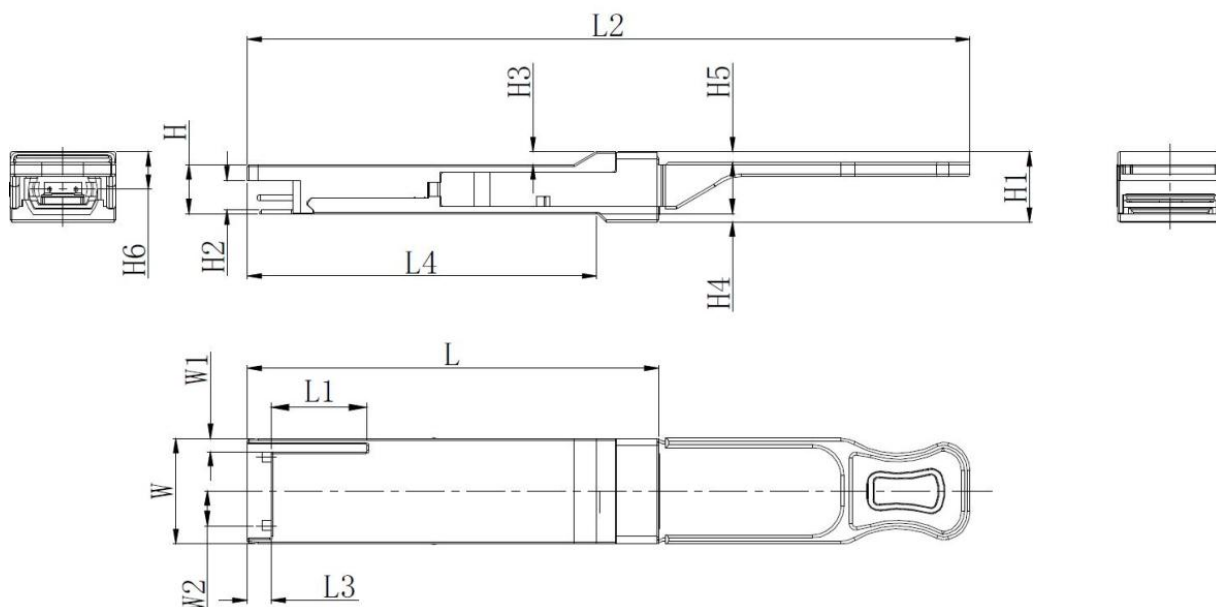
Table 1: QSFP56 Module PIN Definition

Notes:

1. GND is the symbol for signal and supply (power) common for QSFP56 modules. All are common within the QSFP56 module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.
2. VccRx, Vcc1 and VccTx are the receiving and transmission power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown in Figure 3 below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP56 transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

Package Outline

Dimensions are in millimeters. All dimensions are $\pm 0.2\text{mm}$ unless otherwise specified. (Unit: mm)



Regulatory Compliance

Feature	Test	Method
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000V for SFI pins, >2000Vfor other pins.)
Electrostatic Discharge (ESD) Immunity	IEC61000-4-2	Class 2(>4.0kV)
Electromagnetic Interference (EMI)	CISPR22 ITE Class B FCC Class B CENELEC EN55022 VCCI Class 1	Comply with standard
Immunity	IEC61000-4-3	Comply with standard
Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1,2	Compatible with Class I laser Product

Ordering Information

Part. No	Specifications								
	Pack	Rate (Gbps)	Tx (nm)	Po (dBm)	RX	Sen (dBm)	Temp (°C)	Reach (M)	DDM
AC-QS-8G200-01	QSFP56	200G	VCSEL 850	-6.5~4	PIN	<-6.5	0~70	100	Y